In Re Claims

1. (Currently Amended) A system, comprising:

a first write state machine;

a second write state machine;

a pulse generator operable to generate a first current draw waveform ~~pulse~~ of current to the first ~~state~~ write state machine and a second current draw waveform ~~pulse~~ of current to the second write state machine; and

a delay circuit operable to inject a time delay between the first current draw waveform ~~pulse of current~~ and the second current draw waveform~~pulse of current~~, and wherein the time delay is less than a duration of the current draw waveform applied to the first write state machine.

2. (Currently Amended) The system of Claim 1, wherein ~~the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein~~ the first current draw waveform and the second current draw waveform ~~has~~ include a large initial pulse of current followed by a plurality of smaller pulses of current.

3. (Currently Amended) The system of Claim 2, wherein the delay circuit is operable to delay the second current draw waveform~~pulse of current~~ which is applied to the second write state machine for ~~at least as long as~~ the duration of the ~~first~~ large initial pulse of current of the first current draw waveform which is applied to the first write state machine.

4. (Currently Amended) The system of Claim 2, wherein the large initial pulse of current of the second current draw waveform ~~pulse of current~~ which is applied to the second write state machine occurs during a delay between the ~~first~~ large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

5. (Currently Amended) The system of Claim 2, wherein the large initial pulse of current of the second ~~pulse of current~~ current draw waveform which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

6. (Currently Amended) The system of Claim 2, wherein the ~~first~~ large initial pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the large initial ~~second~~ pulse of current which is applied to the second write state machine.

7. (Currently Amended) The system of Claim 2, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the ~~first~~ large initial pulse of current applied to the first write state machine.

8. (Currently Amended) A method, comprising:

applying a first current draw waveform ~~pulse~~ of current to a first write state machine;

delaying a second current draw waveform ~~pulse~~ of current by a predetermined amount of time from a start of the first current draw waveform~~pulse~~; and

applying the second pulse of current to a second write state machine, wherein the predetermined amount of time is less than a duration of the first current draw waveform.

9. (Currently Amended) The method of Claim 8, wherein ~~the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein~~ the first current draw waveform and the second current draw waveform ~~has~~include a large initial pulse of current followed by a plurality of smaller pulses of current.

10. (Currently Amended) The method of Claim 9, wherein the large initial ~~first~~ pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the ~~second~~ large initial pulse of current which is applied to the second write state machine.

11. (Currently Amended) The method of Claim 9, wherein the predetermined amount of time for the delay is ~~at least as long as~~ the duration of the ~~first~~ large initial pulse of current applied to the first write state machine.

12. (Currently Amended) The method of Claim 9, wherein the second ~~pulse~~ current draw waveform of current which is applied to the second write state machine occurs during a delay

between the ~~first~~ large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

13. (Currently Amended) The method of Claim 9, wherein the second current draw waveform ~~pulse~~ of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

14. (Currently Amended) The method of Claim 9, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the ~~first~~ large initial pulse of current applied to the first write state machine.

15. (Currently Amended) A computer-readable medium having computer-executable instructions, comprising:

applying a first current draw waveform ~~pulse~~ of current to a first write state machine;

delaying a second current draw waveform ~~pulse~~ of current by a predetermined amount of time from a start of the first current draw waveform~~pulse~~; and

applying the second pulse of current to a second write state machine, wherein the predetermined amount of time is less than a duration of the first current draw waveform.

16. (Currently Amended) The computer-readable medium of Claim 15, wherein ~~the pulse generator is operable to generate a plurality of pulses of current having a predetermined waveform, wherein~~ the first current draw waveform and the second current draw waveform ~~has~~include a large initial pulse of current followed by a plurality of smaller pulses of current.

17. (Currently Amended) The computer-readable medium of Claim 16, wherein the large initial ~~first~~ pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the ~~second~~ large initial pulse of current which is applied to the second write state machine.

18. (Currently Amended) The computer-readable medium of Claim 16, wherein the predetermined amount of time for the delay is ~~at least as long as~~ the duration of the ~~first~~ large initial pulse of current applied to the first write state machine.

19. (Currently Amended) The computer-readable medium of Claim 16, wherein the second ~~pulse~~ current draw waveform of current which is applied to the second write state machine occurs during a delay between the ~~first~~ large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

20. (Currently Amended) The computer-readable medium of Claim 16, wherein the second current draw waveform ~~pulse~~ of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first

write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

21. (Currently Amended) The computer-readable medium of Claim 16, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the ~~first~~ large initial pulse of current applied to the first write state machine.